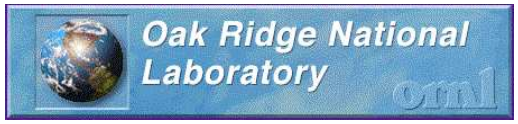
# PVM Research FY'95




**ATM Networks**
Developed PVM version that exploited low level ATM interface
Results showed little gain in actual application performance

**Buffered Tracing**
Tracing is important for debugging and performance tuning
Results showed even modest buffering dramatically improves performance

**Static Groups**
Static groups were considered as an option for future versions of PVM
Results showed significant advantages when PVM runs on MPP

**Multithreading**
Potentially the next major interface change to message passing systems
Presently not portable enough to be incorporated into PVM

**Validation Suite**
Developed a suite of test programs that can be used to validate vendor versions and new releases of PVM

**Shared Memory**
Created a more robust and portable shared memory port with assistance from Convex, SGI, and DEC

**MPP to MPP Performance**
Improved PVM's ability to connect multiple MPP over WAN important to IWAY effort and Virtual Laboratory development

http://www.epm.ornl.gov/pvm